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Customizable Computer System**Abstract**

A customizable computing system, having a microprocessor and a programmable
5 logic device coupled to the microprocessor via a dedicated bus. The programmable logic
device includes a configuration to provide I/O functionality to the system and may be a
field programmable gate array. The programmable logic device operates as both a north
bridge and a south bridge as is understood by those of ordinary skill in the art. Requests
are received from the microprocessor in the programmable logic device over the
10 dedicated bus and are processor specific requests. The processor specific requests are
translated into processor dependent commands by a bridge. After the processor specific
requests are translated into processor dependent commands, the commands are forwarded
to processor independent I/O structures which interface with both internal and external
peripheral devices to the customizable computing system.